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RRR OF COPPER COATING AND LOW TEMPERATURE ELECTRICAL RESISTIVITY OF MATERIAL FOR TTF COUPLERS

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Abstract
In the framework of the R&D program on the TTF III main RF coupler, IPN Orsay developed in close collaboration with LAL institute, a dedicated facility for the electrical characterization of different materials at low temperature. This apparatus was used for measuring the electrical resistivity versus temperature (4.2 K- 300K) of various samples produced in the industry. These tests were performed in order to compare the RRR of the samples, qualify and find the optimum parameters for the coating process. Seven flat samples were tested in a saturated liquid helium bath under ~1013 mbar pressure: measurements were performed on bare 316L samples, nickel coated 316L samples, and copper coated 316L samples with a nickel under layer. We investigated, in particular, the effect of vacuum annealing at 400°C on the RRR of the copper coating. Our experimental data are compared to previous measurements reported by other groups, and theoretical results (e.g. Gruneisen-Bloch equation) and a good agreement was found. Finally, the tested samples fulfill the TTF III coupler design parameters requirements in terms of heat loads to the refrigerator at 2 K, 4 K and 70 K.

INTRODUCTION
The power coupler is a crucial component for operating Superconducting RF (SRF) cavities. The main function of this device is the efficient transfer, in matched condition, of the RF power from the source to the particles beam. This complex device operates in stringent conditions: 1) it should handle and transmit a high RF power (~250 kW-500 kW) through a ceramic window, 2) it is an interface between warm parts of the accelerating cryomodule at room temperature (T~300K) and cold parts at cryogenic temperatures (i.e. T<100 K), 3) it is also an interface between atmospheric pressure in the wave guide operating at room temperature and ultrahigh vacuum (<10⁻⁸ mbar) in the SRF cavity. Due to such operating conditions, the RF power coupler should be carefully designed in order to achieve reliably the required performance. For a large superconducting linac such as TESLA (actually ILC; International Linear Collider) or XFEL [1-3], it is mandatory to minimize the cryogenic power needed for the machine. Consequently, the static and dynamic heat load contributions of the power coupler to the cryomodule must be kept as low as possible. In the framework of the CARE-SRF project WP7 supported by EU, LAL and DESY laboratories launched an R&D program aimed at the development and fabrication in the industry of forty TTFIII power couplers [4-9]. In this program, the development of high performance copper coating for the different parts of the power coupler (e.g. inner conductor, outer conductor, bellows) is of prime importance for reducing the cryogenic thermal budget to the refrigerator operating at a T= 2 K.

MAIN DESIGN PARAMETERS OF TTF3 COUPLER AND THERMAL SPECIFICATIONS
The TTF III power coupler (Fig. 1) is of coaxial type with two cylindrical alumina windows: 1) a warm window, operating at T≥300 K, and located at the waveguide to coax transition (upper part), 2) a cold window, located at the lower part and thermally anchored to the infrared radiation shield at T≤ 70 K.

![Fig. 1: Drawing of the TTF III power coupler](image-url)
reduce the heat load to the cavity at T=2 K. Furthermore, the TTF III power coupler should handle and transmit efficiently a high RF power in pulsed mode (e.g. peak RF power: 250 kW, average power of 3.2 kW for a pulse length \( \tau_p = 1300 \mu s \) at a repetition rate of 10 Hz) to the electrons or positrons beam with minimum heat loads to the different cooling circuits of the refrigerator (e.g. T=2K, T=4.2K and T=70K). The main design parameters, which have a major effect on the thermal performance of the power coupler and the cryomodule, are illustrated in Table 1.

Table 1: Main design parameters of TTF III coupler and thermal specifications

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Specification</th>
</tr>
</thead>
<tbody>
<tr>
<td>Frequency (MHz)</td>
<td>1300</td>
</tr>
<tr>
<td>Operating mode</td>
<td>Pulsed, total pulse length: ( \tau_p = 1300 \mu s ), rise time ( \tau_R = 500 \mu s ), flat top with beam ( \tau_{FTop} = 800 \mu s )</td>
</tr>
<tr>
<td>Heat load @ 2 K (mW)</td>
<td>60</td>
</tr>
<tr>
<td>Heat load @ 4 K (mW)</td>
<td>500</td>
</tr>
<tr>
<td>Heat load @ 70 K (W)</td>
<td>6</td>
</tr>
<tr>
<td>Peak RF power (kW)</td>
<td>250</td>
</tr>
<tr>
<td>Average RF power (kW)</td>
<td>3.2</td>
</tr>
<tr>
<td>Repetition rate (Hz)</td>
<td>10</td>
</tr>
</tbody>
</table>

Copper coating specifications for the TTF-3 couplers

Among others items, the copper coating of the coupler is an important task in order to achieve the required RF and thermal performance. The optimum values of the coating characteristics (e.g. thickness \( \varepsilon_{Cu} \), purity, low hydrogen content, residual electrical resistivity \( \rho_R \), low temperature thermal conductivity \( k_{Cu}(T) \), low temperature surface resistance \( R_S(T) \), adhesion on stainless steel substrate and surface roughness \( R_s \)) are a compromise between several criteria. More precisely, the heat load \( Q_{Cold} \) to the 2 K circuit should be low and have two contributions, namely the static part \( Q_{cond} \) and the dynamic part \( Q_{RF} \). The static part is due to solid conduction and is simply given by the expression:

\[
Q_{cond} = \frac{A_{Cu}}{L_{eff} \rho_R} \int \frac{k_{Cu}(T)}{\varepsilon_{Cu}} dT (1)
\]

With:
- \( A_{Cu} \): area of the copper coating in the heat flow direction (\( A_{Cu} = \pi.D_{eff} \varepsilon_{Cu} \)),
- \( D_{eff} \): effective diameter of the coated substrate (pipe or bellow),
- \( L_{eff} \): effective length of the conduction thermal path,
- \( TC \): cold temperature,
- \( TH \): hot temperature.

The dynamic part \( Q_{RF} \) is due to Joule heating of the copper coating by the RF surface magnetic field \( H_S \):

\[
Q_{RF} = \frac{1}{2} \int_{S_{Cu}} R_S(T).H_S^2 dS \quad (2)
\]

From the above relationship (1), the conduction heat flux \( Q_{Cold} \) is proportional to both the thickness and the mean thermal conductivity of the copper coating \( Q_{cond} \propto A_{Cu} \cdot k_{Cu}(T) \cdot \varepsilon_{Cu} \cdot k_{Cu} \). Then to reduce \( Q_{Cold} \), \( k_{Cu} \) should be lowered (i.e. the purity of the copper should be low) as well as its thickness. On the contrary, to reduce the RF losses \( Q_{RF} \), \( R_S(T) \) should be decreased: 1) in the normal skin effect regime, the low temperature surface resistance \( R_S \) is proportional to the square root of the residual DC resistivity \( \rho_R \) (e.g. \( R_{90 K} \rho_R^{0.5} \)), hence \( \rho_R \) should be low or namely copper purity should be high, 2) in the anomalous skin effect regime, the low temperature surface resistance \( R_S \) is independent of copper purity or \( \rho_R \) (for copper at TESLA frequency \( f=1300 \) MHz, \( R_{90K}=1.3 \) m\( \Omega \)). Now it is clear that the two previous criteria are contradictory: more precisely to reduce \( Q_{Cold} \) (respectively \( Q_{RF} \)) we should use low purity (respectively high purity) copper coating. As these two criteria go to reverse direction, there is an optimum value of \( \rho_R \) or coating RRR (Residual Resistivity Ratio). The RRR is defined as the ratio between electrical resistivity of the material \( \rho(T=273K) \) at the ice point and the residual electrical resistivity \( \rho_R \) measured at the liquid helium normal boiling point \( T=4.22 K \):

\[
RRR = \frac{\rho(T=273K)}{\rho_R} \quad (3a)
\]

Furthermore, the high temperature (i.e. \( T \approx 300 K \)) electrical resistivity is weakly dependent on the material purity [10]. Consequently, we adapted through this paper the following expression of RRR:

\[
RRR \approx \frac{\rho(T=300K)}{\rho_R} \quad (3b)
\]

In the above expression (3b), the room temperature resistivity \( \rho(T=300K) \) is used instead of the resistivity at ice point \( \rho(T=273K) \).

The following specifications and preparation procedure of the copper coatings are needed to achieve reliably the required RF and thermal performance (Table 1) of the TTF III power coupler:

1) Copper coated stainless steel is of austenitic grade AISI 316L (EN 1.4435),
2) Stainless steel parts are Hydrogen degassed at 950°C for 2 hours,
3) Thicknesses are respectively 30 µm ± 10µm for the Inner Conductor (IC) and 10 µm ± 5µm for the Outer Conductor (OC) copper coatings,
4) Low hydrogen content,
5) Ni flash under layer thickness ≤ 1 µm
6) Electrical resistivity: RRR ≥ 30 after baking at 400°C during 1 h in a vacuum furnace.

The effect of the copper coating RRR on the heat loads, at cryogenic temperature for the three thermal intercepts, was computed previously [11] at an average RF power of 10kW leading to the results summarized in Table 2. These results clearly show that a RRR of the copper coating higher than 30 fulfills the required thermal performance at 2 K and 4 K with a sufficient safety margin. Note that the heat loads are nearly proportional to the average RF power in the coupler. It should be stressed that the values of skin depth $\delta_{Cu}$ (e.g. normal skin effect regime or anomalous skin effect regime), in which RF current is concentrated on the external surface of the coating during coupler operation at $f=1300$MHz, are lower than 2µm for 2 K ≤ T ≤ 300 K and copper RRR in the range 10-100. Consequently, there is no RF current in the stainless steel substrate of the coupler components. Further, this value of $\delta_{Cu}$ is at least 5 times lower than the minimum copper coating thickness.

Table 2: Effect of the copper coating RRR on the heat loads at cryogenic temperature for the three thermal intercepts- Average RF power of 10kW [11]

<table>
<thead>
<tr>
<th>Thick (µm)</th>
<th>Conduc.</th>
<th>RRR</th>
<th>Load @2K IC&amp;OC (mW)</th>
<th>Load @ 4K IC&amp;OC (mW)</th>
<th>Load @ 70 K (W)</th>
</tr>
</thead>
<tbody>
<tr>
<td>30 IC</td>
<td>10</td>
<td>10</td>
<td>10.</td>
<td>10.</td>
<td>10.</td>
</tr>
<tr>
<td>10 OC</td>
<td>10</td>
<td>110</td>
<td>680</td>
<td>3.6</td>
<td>9.7</td>
</tr>
<tr>
<td>30 IC</td>
<td>100</td>
<td>100</td>
<td>130</td>
<td>730</td>
<td>3.1</td>
</tr>
<tr>
<td>10 OC</td>
<td>100</td>
<td>100</td>
<td>130</td>
<td>730</td>
<td>3.1</td>
</tr>
</tbody>
</table>

COPPER COATING TECHNIQUE AND ROOM TEMPERATURE CHARACTERIZATION OF SAMPLES

Two main techniques [12] could be used for coating stainless substrate with high purity copper: a) electrolytic deposition, b) magnetron sputtering. We decided to use the first method because, as compared to the second one this technique, it allows the production of high material performance in much easier manner. Briefly, the electrolytic coating process is performed in bath which contains typically the following impurities:

- Chromium: 0.04 ppm,
- Cobalt < 0.04 ppm (detection limit),
- Iron: 21.8 ppm,
- Manganese: 0.01 ppm (detection limit).

Note that the copper plating process of the samples and coupler parts was done in the industry. As the details of the process are considered to be proprietary of the industry, they could not be discussed in the present paper.

Copper coating thickness measurement

Non destructive measurements of thickness profiles were performed by X-ray fluorescence method. FischerScope® XRAY XDL device was used for this purpose. Five copper coated samples with a specified thickness $e_{Cu}=30$ µm were tested (Fig.2). This histogram shows that: a) the mean value of the coating thickness is $<e_{Cu}>=33.68$ µm with a standard deviation $\sigma_{eCu}=4.3$ µm and a range of 10.3 µm, b) $\sigma_{eCu}$ is within the specified tolerances of ± 20% (i.e. ±6 µm), c) out of the five samples tested, only the sample #3 is outside the specified range. Moreover, the micrographs showing the copper coating thickness distribution in the bellows is presented in Fig.3. The measured values 28 µm - 34 µm are well in the specification range (30 µm ± 10µm).

Fig. 2: Histogram of the thickness measured by X-Ray fluorescence

Fig. 3: Copper coating thickness distribution in some locations of the small bellow.

Copper coating parts of TTF3 coupler

The copper coating characteristics produced in the industry achieved the required properties, so we decided to coat the different parts of the TTF3 coupler (Fig.4).

Fig. 4: Photographs of the different copper-plated parts of TTF3 coupler
ELECTRICAL RESISTIVITY AND RRR MEASUREMENT

Experimental set-up and procedure

The measurements were performed using the standard DC four probes method (Fig. 5). The electrodes are clamped to the flat test sample by means of copper-beryllium springs: the resulting contact pressure between the electrodes tips and the sample is ~12 Bars. A Kapton foil of ~0.1 mm thickness, sandwiched between the sample and electrodes support, allows their electrical insulation from the sample. Finally the distance between the voltage electrodes is ~60 mm. The test sample, which is equipped with a calibrated [13] Cernox thermometer (T=1.5K – 300 K), is then immersed in a saturated liquid helium bath (Pressure: P= 100 kPa, T= 4.2K).

The sensing current, which is delivered by a precise standard DC voltage supply and set to a value close to 1.5 A, is maintained constant during the whole test and on-line measured via the voltage drop across a precision resistor (R= 1 Ω, ΔR/R~10^{-4}).

The different steps of the experiment are the following:

a) Perform 10 measurements at T=4.2 K, each measurement consisting of 15 dual scans in the direct (+V) and reverse direction (-V),

b) Vaporize the liquid helium using a bath heater,

c) Heat the sample in a controlled way to maintain it at a given constant temperature T in the range 4.2 K- 300 K,

d) Once T is stable, perform 10 measurements (5 to 10 dual scans),

e) This process (c-d) is repeated as function of temperature from 4.2 K to 300 K.

Note that, as the measured signal is very small (i.e. 45 μV-450 μV), the change of the sensing current direction is mandatory in order to eliminate parasitic thermal voltages.

Results and discussion

Due to the very low thickness (~10-30 μm) of the copper coating and the electrical contact type between sample and measuring electrodes (no soldering, contact by pressure), it is not possible to measure directly the resistance of the copper coating alone and especially at cryogenic temperatures: the sample could be broken or damaged. Moreover, the coating electrical resistivity should be measured in real operating conditions (i.e. with the stainless steel substrate): the coating thermo-mechanical stress has an influence on the electrical resistivity. Consequently, the measurement method is by comparison: we measured both coated and uncoated samples including those with only the nickel under layer coating, which is sandwiched between the stainless steel substrate and the copper coating. Further, in our knowledge, only few precise results of the electrical resistivity of Stainless Steel (SS) at low temperature were previously published; it is then interesting to get such data. Six coated and uncoated samples (Table 3) were tested. Notice that for all the coated samples, the thickness of the nickel under layer is 1-2μm. The sample #1 was tested twice: a) as received, b) after annealing at 400°C in a vacuum furnace. The sample #4 was also tested twice: a) as received, b) after chemical removal of the nickel under layer.

Table 3: Description of the tested samples

<table>
<thead>
<tr>
<th>Sample #</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>as received SS + Ni under layer coating+ copper coating: double sided (30µm/side)</td>
</tr>
<tr>
<td>1′</td>
<td>after vacuum annealing (400°C, 1h00) SS + double sided copper coating (30µm/side)</td>
</tr>
<tr>
<td>3</td>
<td>Bare 316L Stainless Steel</td>
</tr>
<tr>
<td>4</td>
<td>SS + double sided Ni under layer coating</td>
</tr>
<tr>
<td>4′</td>
<td>Sample #4 after chemical removal of Ni</td>
</tr>
<tr>
<td>5</td>
<td>SS + Ni under layer coating+ copper coating: double sided (33µm/side)</td>
</tr>
<tr>
<td>6</td>
<td>SS + Ni under layer coating+ copper coating: double sided (33µm/side)</td>
</tr>
</tbody>
</table>

The histogram of ten measurements of the electrical resistance of the copper plated sample #1 R_{Samp.1} at T=4.2K is shown in Fig. 6. These data lead to a mean value of the sample resistance R_{Samp.1} =31.8 μΩ, with a relative standard deviation ~ 5.10^{-3} and a range ΔR_{Samp.1}=0.51 μΩ. Moreover, the variations of the sample resistance with temperature are presented in Fig. 7. As expected, the data show the well-known monotonic and strong decrease of R_{Samp.1} with T from 285μΩ at 286 K
down to 31.8 $\mu \Omega$ at 4.2 K: this behaviour is typical of medium and high purity metals [10].

Furthermore, the results of Fig. 7 clearly show that the overall electrical conductance $1/R_{\text{sample}}$ of the sample is dominated by the conductance of the copper coating $2.\rho_{\text{Cu}}/\rho_{\text{Cu}}$: at $T=4.2$ K, $\rho_{\text{Cu}}$ is more than factor 300 lower than the electrical resistivity of stainless steel $\rho_{\text{SS}}$. More precisely: a) for RRR=9.6 copper, $\rho_{\text{Cu}}=1.635 \times 10^8 \Omega$.m @ 300 K and $\rho_{\text{Cu}}=1.71 \times 10^7 \Omega$.m at $T=4.2$ K, b) for RRR=27 copper, $\rho_{\text{Cu}}=1.54 \times 10^8 \Omega$.m at $T=300$ K and $\rho_{\text{Cu}}=5.71 \times 10^8 \Omega$.m at $T=4.2$ K.

The variations of the electrical resistivity as function of the temperature for 316L stainless steel are illustrated in Fig. 8. This curve was simply deduced from the measurements of the sample resistance $R_{\text{sample}}$ according to the well-known expression:

$$\rho_{\text{SS}} = \frac{L}{e_{\text{SS}}}R_{\text{Sample}}$$

Where:
- $L$, $l$ and $e_{\text{SS}}$ are respectively the distance between the voltage electrodes, the sample width and thickness.

From the above relationship, it is straightforward to derive the expression of the uncertainty on the stainless steel electrical resistivity:

$$\frac{\Delta \rho_{\text{SS}}}{\rho_{\text{SS}}} = \frac{\Delta R_{\text{sample}}}{R_{\text{sample}}} + \frac{\Delta e_{\text{SS}}}{e_{\text{SS}}} + \frac{\Delta l}{l} + \frac{\Delta L}{L}$$

By introducing the different contributions in the above relationship we obtained an uncertainty $\Delta \rho_{\text{SS}}/\rho_{\text{SS}}=6\%$.

This value is close to the observed difference ($\pm 8\%$) between the two stainless steel samples #3 and #4A we tested. The remaining deviation of 2% could be attributed to variations in the chemical composition from sample to sample. Moreover, our experimental data are in a very good agreement with earlier results obtained by Clark et al. [14] who tested four different samples. Note that Clark performed the measurements at only five fixed thermometric points, namely in ice-liquid water mixture, normal boiling points of nitrogen, hydrogen and helium respectively.

The double side coated sample could be simply modelled as a network of five resistances in parallel: one for stainless steel substrate, two for the copper coating and two for the nickel under layer. The equivalent electrical conductance of the sample is simply:

$$\frac{1}{R_{\text{Sample}}} = \frac{L}{\rho_{\text{SS}}} \left( \frac{e_{\text{SS}}}{\rho_{\text{Cu}}} + \frac{2.\rho_{\text{Ni}}}{\rho_{\text{Cu}}} \right)$$

Where:
- $e_{\text{SS}}$ and $\rho_{\text{Ni}}$ are respectively the thickness and the electrical resistivity of the nickel under layer.

The results obtained with samples #3, # 4 and #5 are illustrated in Fig.9.
These data clearly show the effect of nickel and copper on the electrical resistance of the sample. Note that the contribution of the nickel layer to the overall resistance of the sample, thought is measurable, is small as compared to that of copper. This is simply due to 2 factors: a) the very low thickness (~1.5 µm) of Ni as compared to that of Cu (~30 µm) and substrate (~1.5 mm), b) the high electrical resistivity of the nickel sub layer, which is probably dirty or alloyed.

Using equation 6 and the data of Fig.9 we have deduced the electrical resistivity of the copper coating (Fig. 10).

The estimated error for $\rho_{Cu}$ is 12 % at $T$=4.2 K. Our experimental data are compared to the theoretical curve $\rho_{Cu}$ versus $T$. This theoretical curve was calculated assuming the Matthiessen’s rule:

$$\rho_{Cu}(T) = \rho_i(T) + \rho_R$$  \hspace{1cm} (7)

Where the intrinsic electrical resistivity $\rho_i(T)$ of ideally pure copper material, which is due to thermal scattering (e.g. simple free-electron model of a Debye solid), is given by the well-known Gruneisen-Bloch equation [15]:

$$\rho_i(T) = 4.226 \left( \frac{T}{\theta} \right)^5 \cdot \rho_{\theta}(T) \cdot J_5 \left( \frac{\theta}{T} \right)$$  \hspace{1cm} (8)

The characteristic temperature $\theta$ of the solid phonons spectrum depends on the material. The function $J_5$ is the integral expressed as:

$$J_5 \left( \frac{\theta}{T} \right) = \int_0^{\theta/T} \frac{x^5}{e^x - 1} \frac{dx}{1 - e^{-x}}$$  \hspace{1cm} (9)

For the copper, the theoretical Debye temperature is $\theta_0=310$ K. However, the experimental values reported for $\theta_0$ depends on $T$, especially for $T<200$ K; they are in the range 310 K-350 K [15]. The best fit to our experimental data, according to the Gruneisen-Bloch equation, was obtained with $\theta=340$ K and copper RRR=25. Further the resolution of the electronics used for the tests is not sufficient for the precise measurement of the electrical resistivity of the nickel sub layer. Consequently we have to assume reasonable values of this parameter in order to deduce the RRR of copper coating from our experimental data. The summary of RRR measurements for all the samples tested are illustrated in Table 4: the estimated error on RRR measurement is 24 %.

For the samples without any heat treatment (i.e. as received), the RRR of Cu coating are in the range 20-46 if we use a realistic value of Ni RRR (i.e. RRRNi≈1). Moreover the RRR data of sample #1 are in good agreement with the Gruneisen-Bloch equation: the measured value of copper coating (RRR≈20) is consistent with that given by the theory (RRR≈25).

Table 4: Summary of copper coating RRR results

<table>
<thead>
<tr>
<th>Sample</th>
<th>Ni effect neglected</th>
<th>RRRNi =1</th>
<th>RRRNi =300</th>
</tr>
</thead>
<tbody>
<tr>
<td>#1 As received</td>
<td>19.8</td>
<td>20.4</td>
<td>11.2</td>
</tr>
<tr>
<td>#1 Vacuum Annealed@400°C during 1h00</td>
<td>113</td>
<td>117</td>
<td>107</td>
</tr>
<tr>
<td>#5 As received</td>
<td>23.7</td>
<td>24</td>
<td>21</td>
</tr>
<tr>
<td>#6 As received</td>
<td>45.5</td>
<td>45.9</td>
<td>43</td>
</tr>
</tbody>
</table>

The team of Benvenuti studied the properties of copper coatings developed for the main couplers of LEP2 accelerator [12]. Their data concerning as produced (i.e. before heat treatment) copper coatings are respectively RRR=90 (pyrophosphate electrolytic bath) and RRR=110 (sulphate electrolytic bath) for electroplated samples of 30µm Cu thickness: the higher RRR obtained with sulphate electrolytic bath is attributed to the larger grain size observed [12]. Moreover, the values measured by Benvenuti are 2 to 5 time higher than our data. However, as compared to data obtained at DESY, our RRR results are much higher: Singer et al. [16] measured RRR≈15 for non annealed 5-10 µm thick copper coatings.

Furthermore, the vacuum annealing at 400°C during one hour increases the RRR of copper coating by a factor ~6. This improvement by a factor of 6 is two times higher than that observed by Benvenuti who measured RRR=300 for an electroplated sample (initial RRR=110) after vacuum annealing at 400°C during 2 hours. The significant improvement of the RRR of the copper is mainly due to 2 factors: a) the re-crystallization and the resulting increase of grain size by annealing observed by microstructure analysis [16], b) out gassing. Note that the optimum vacuum annealing temperature is in the range 350°C -400°C for a baking duration $\tau_{annealing}$=1-2 hours. It should be stressed that to keep the diffusion depth $\delta_{diff}$ of nickel or iron into copper very much lower than copper thickness, the annealing temperature should be lower than 400°C [16]. More precisely, $\delta_{diff} \propto (\tau_{annealing} - D)^{1/2}$. The
diffusion coefficient $D$ depends on the absolute annealing temperature $T_{\text{annealing}}$ (e.g. $D \sim \exp(-\Delta H/R T_{\text{annealing}}$) where $\Delta H$ and $R$ are respectively the enthalpy of the diffusion process and $R$ the gas constant.

**CONCLUSION**

In the frame of the CARE-SRF project WP7 R&D program aimed at development and fabrication in the industry of thirty TTF III power couplers, we designed an apparatus dedicated to the measurement of the electrical resistivity of materials at low temperatures. The electrical resistivity of different materials (stainless steel, Cu coating) were measured in the range 4.2 K – 300 K. The RRR of Cu coating was deduced from these data: 1) for as received samples the RRR values are in the range 20-46, 2) the vacuum annealing at 400°C during one hour increases the RRR of copper coating by a factor ~6. Moreover, our electrical resistivity data are compared to previous results reported by other groups for stainless and a good agreement was found. Our measured electrical resistivity data versus temperature for copper coatings are well fitted by the theoretical Gruneisen-Bloch equation. Finally, the tested samples fulfil the TTF III design parameters requirements in terms of heat loads to the refrigerator at 2 K, 4 K, and 70 K.

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**REFERENCES**


