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► **To cite this version:**

P. Delpierre, J-F. Berar, L. Blanquart, N. Boudet, P. Breugnon, et al.. Large Surface X-Ray Pixel Detector. 2001 IEEE Nuclear Science Symposium (NSS) and Medical Imaging Conference (MIC), Nov 2001, San Diego, United States. N23.2-4 p. in2p3-00012293

**HAL Id: in2p3-00012293**

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Submitted on 30 Jul 2002

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# Large Surface X-Ray Pixel Detector

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**Abstract:** A large surface semi-conductor photon counting pixel detector is being built. As a first step, this detector is optimized for X-ray crystallography. The aim is to provide a high dynamic range ( $>10^9$ ), high counting rate ( $10^7$  ph/s per pixel) and fast image readout (3 ms). A full custom chip has been produced and tested, and results will be presented. Prototypes of  $6\text{ cm}^2$  with pixels of  $330\text{ }\mu\text{m} \times 330\text{ }\mu\text{m}$  have been built together with a full fast readout system. These prototypes have been tested in the D2am beam line at the ESRF synchrotron (Grenoble, France), for different photon energies (10 to 24 KeV). The signal is well out of the noise until 10 KeV. We will give results on the efficiency as a function of the photon rate and the effect of the threshold on the efficiency between the pixels. Furthermore, to demonstrate the large dynamic range, we made diffraction images. We will then describe the  $6.4\text{ cm} \times 6.4\text{ cm}$  camera that we are building and our future plans.

## I. INTRODUCTION

PHOTON counting pixel detectors are being investigated as a solution to improve CCD's regarding the dynamic range, the image readout duration, and to suppress the noise with an adjustable threshold. However, photon counting is limited in rate while CCD's are not. A full custom chip has been built with special efforts to push the counting rate as high as possible. Part of our group has a long experience to build large surface pixel detectors for High Energy Physics experiments [1,2]. We plan to use the same technique to build large surface X-ray detectors ( $6\text{ cm} \times 6\text{ cm}$  or more). To start with, we have built  $4\text{ cm} \times 1.6\text{ cm}$  prototypes. They have been tested in an x-ray beam and results will be reported. We will also describe the detector that we are building for the D2AM beam line of the ESRF synchrotron (Grenoble, France).

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Manuscript received in November 7, 2001.

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## II. THE CHALLENGE

The aim is to build a crystallographer without the limitations of the current CCD based detectors, that is:

- needs low temperature to reduce the noise (the noise is integrated during the readout)
- the dynamics is limited to about 14-bit ( $1.6 \cdot 10^4$ )
- for the x-ray detection a light converter is mandatory
- a fast shutter is needed to control the data taking time

With pixel detector the challenge is the following:

- sensitivity : 1 ph / pixel (needed to see satellite peaks)
- noise :  $< 1$  ph /hour per pixels
- dynamic range  $> 10^9$  (32-bit counters)
- electronic control of data taking time at a few 100 ns
- counting rate from  $10^{-2}$  to  $10^7$  photons/second/pixel
- dimensions :  $8\text{ cm} \times 8\text{ cm}$
- real time study :  $< 3$  ms/frame, 128 frames local memory
- thickness  $< 1\text{ mm}$  (equivalent silicon)

## III. FIRST PROTOTYPES

To study the feasibility of an x-rays pixel detector, we have built prototypes. A pixel detector includes a sensor substrate segmented in pixels on which electronic chips are bump-bonded. For each pixel, the electronic chip includes one analog cell followed by a discriminator, and readout logic.

For the first prototypes we have used existing sensors from a physics experiment [1] (DELPHI, CERN, Geneva). The pixel size is  $330\text{ }\mu\text{m} \times 330\text{ }\mu\text{m}$ , which is close to the usual X-ray sources. The size is  $4\text{ cm} \times 1.6\text{ cm}$ .

We had to build dedicated electronic chips, since the ones from physics experiments are optimized for selective readout and not for photon counting. This chip [3] (the XPAD) has been designed to satisfy the characteristics described above. It has been done in the standard technology AMS  $0.8\text{ }\mu\text{m}$  ("Austria Mikro System"). Special care was taken to reach a high counting rate. The threshold can be adjusted pixel by pixel. The pixels can be disabled individually. Each pixel includes a 16-bit counter which can be read during the exposure.

Ten chips have been bump-bonded (by Tronics, Grenoble) on each of 8 sensors. One of them is shown on the Fig.1. These pixel modules have been glued on printed circuit boards for tests.

An external readout and memory board has been build. This board includes an Altera programmable chip and memories for 128 images of 32-bit per pixel. During the exposure the system scans (at 33 MHz) the pixels 16-bit counters overflows and add them to the corresponding 16-bit counter in the readout and memory board. At the end of exposure the full contains of the pixel counters are read and add as lower significant bits. The total readout time is 3 ms and it can be reduced if necessary.

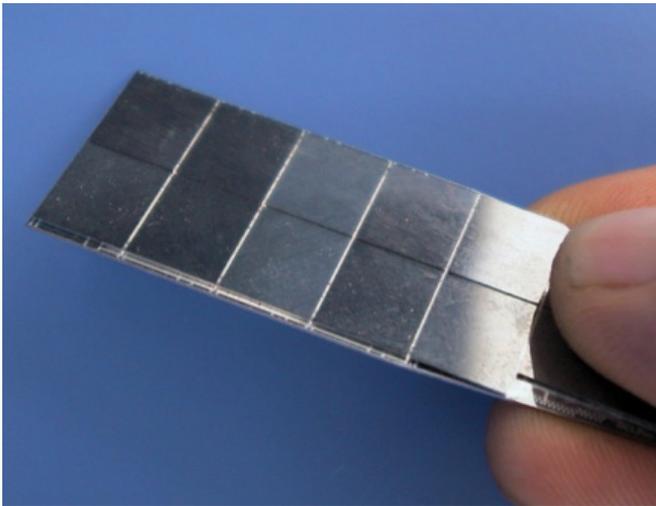


Fig. 1. Pixel module, 10 XPAD chips are bump-bonded on a silicon sensor.

#### IV. ELECTRONIC CHIP TEST RESULTS

The electronic chip has been tested before to be mounted on the sensor. The noise is 200 e<sup>-</sup> RMS. The maximum rate

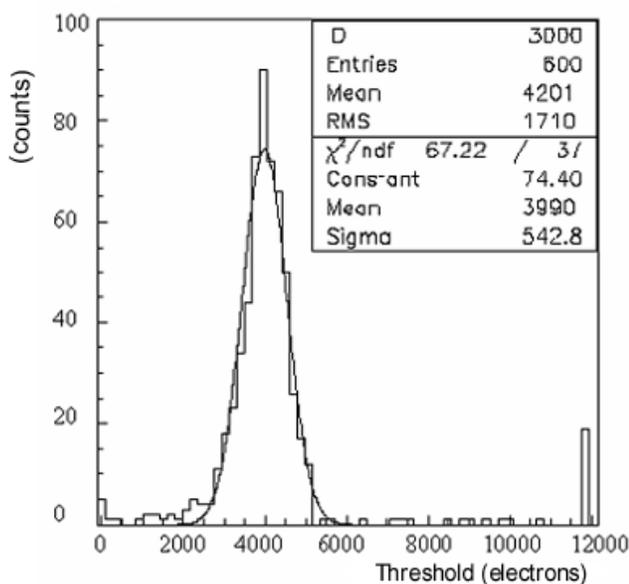


Fig. 2. Threshold distribution for one chip (600 pixels)

has been measured by the mean of the analog test input. The pulse width is 50 ns FWHM, then, in recurrent mode, a rate of 10<sup>7</sup> pulses/second was easily reached.

After mounting on the sensor, the input capacitance is much higher (about 800 fF) and the measured noise is 350 e<sup>-</sup>. To be able to work at very low threshold, the two other important parameters are the crosstalk and the threshold spread. The crosstalk was measured and it is 0.4 %, which is pretty good. The threshold spread is 540 e<sup>-</sup>. This is large but acceptable. However there is a tail of out of range pixels, as it can be seen in the threshold distribution on the Fig.2. This is a defect that we understand now and it will be corrected in the next version of electronic chip. For the following tests, these pixels have been disabled.

#### V. PROTOTYPE TEST RESULTS

The modules have been tested in the ESRF-D2AM beam line. From threshold curves we could calibrate the detector as a function of the x-ray energy. The derivative of the threshold curves are shown in the Fig.3. The signal is well separated from the noise until 10 keV. The energy resolution is 1 MeV.

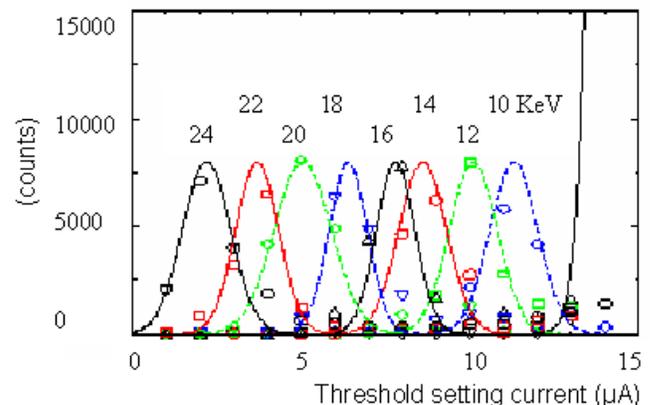


Fig. 3. Energy calibration, the resolution is about 1 MeV.

One important measurement for photon counting pixel detectors is the efficiency between the pixels. If the threshold is too high, we lose photons because the charges are shared between the pixels and the part on each one can be lower than the threshold. If the threshold is too low, the charges can be above the threshold for both pixels and we have a double counting.

This measurement has been done on several pixels and the results are in the Fig.4. For this measurement the beam has been tuned to 10 µm. As one can expect, the correct setting of the threshold corresponds to the half of the charges deposited by the photon. For a beam of 25 keV, the charge from the photon conversion is around 7000 electrons and one can see that with a threshold of 3500 electrons the distribution becomes uniform.

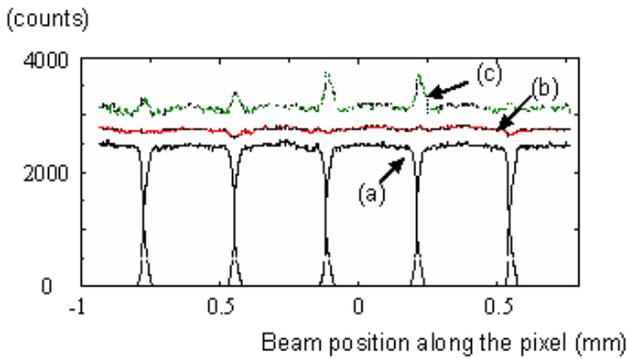


Fig. 4. Counting efficiency for a 10µm beam scan along the pixels.

- (a) Counting per pixels, threshold = 3500 e<sup>-</sup>
- (b) Total counting, threshold = 3500 e<sup>-</sup>
- (c) Total counting, threshold = 3200 e<sup>-</sup>

The counting rate was measured in the 2/3 filling mode of the ESRF beam. That means that the real counting rate is 1.5 times higher than measured. The result is in the Fig 5. It is linear until  $10^5$  ph/s per pixel and the maximum measured rate is around  $1 \times 10^6$  ph/s per pixel. Then the maximum real counting rate is  $1.5 \times 10^6$  ph/s per pixel. The noise is less than one count per hour per pixel and we can measure rates as low as 0.01 ph/s per pixel. Also on Fig. 5, one can see that the neighboring pixel which receives few photons from the beam halo, is not disturbed by the large charge on its neighbor while it would have been with the CDD by "blooming effect".

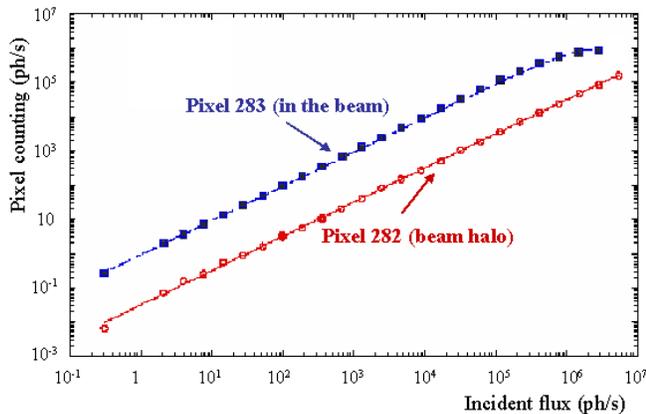


Fig. 5. Counting rate linearity. The beam duty cycle is 2/3, and then the real rate is 1.5 times more.

To have a first idea of the behavior of pixel detectors in a real X-ray experiment the detector was mounted on a SAX (Small Angle X-ray scattering) table.

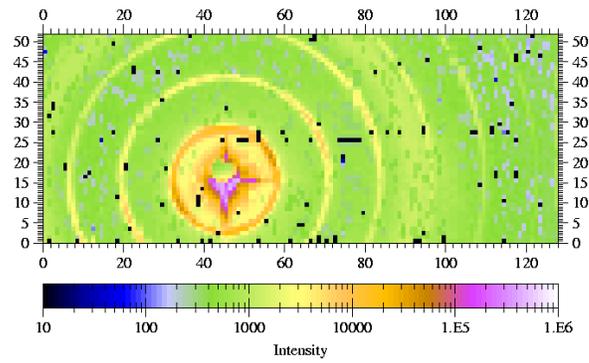


Fig.6. Diffraction image from a 20 keV X-ray beam on a silver behenate sample. The shadow between the 3rd and the 4th ring is due to the diffusion on the module cover. The dark points are disabled pixels (threshold out of range).

A silver behenate sample was installed and exposed during 400 seconds with a thin 20 keV Xray beam. The resulting image is represented in the Fig.6. We can measure up to 7 order of diffraction with a dynamic in intensity of 5 orders of magnitude.

However, on this picture, one can see black spots. This corresponds to the pixels with threshold out of range which have been disabled, as it was explained above (see the paragraph IV). The shadow between the 3<sup>rd</sup> and the 4<sup>th</sup> rings is due to the diffusion on the plastic cover which protects the detector.

## VI. NEW PROTOTYPES

To correct the threshold spread a new pixel chip (XPAD-2) has been designed. It has already been submitted in AMS. (0.8 µm). The defect in the analog cell has been corrected and the threshold tuning range has been enlarged.

We have also designed sensors adapted to this application. The pixel contact pads are much smaller (60 µm of diameter instead of 140µm for the DELPHI sensors). That will reduce the input capacitance and then the noise. That will also reduce the pulse width so that the maximum counting rate will be increased.

The sensor wafers (4") include two sizes of chips. One is similar to the first prototypes (4 cm x 1.6 cm) the other is 6.4 cm x 0.8 cm. These last chips will serve to build a 6.4 cm x 6.4 cm detector plate. They will be glued and wire bonded on thin ceramics boards for the routing of the signals to the connectors for readout and memory boards. These detector modules will be mounted on an air cooled mechanical support (see the Fig. 7). Readout and memory boards based on the prototype that we have used for the tests will be built and mounted on each tile. The tiles will be linked by a flexible cable to a PC interface board.

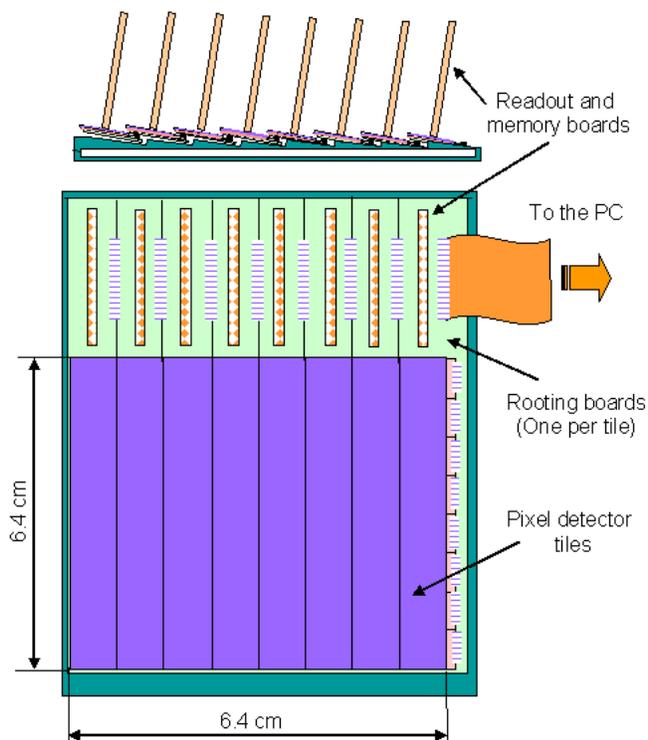


Fig.7. Complete pixel crystallographer prototype.

## VII. PERSPECTIVES

We plan to build a radiation hard chip in Deep Sub Micronics technology, starting early next year. We will use the same schematic but the pixel size will be around  $150\ \mu\text{m} \times 150\ \mu\text{m}$ . These chips will be assembled on radiation hard silicon sensors close to the design of the ATLAS experiment pixel sensors [4]. They will be sized to build a radiation hard  $8\ \text{cm} \times 8\ \text{cm}$  crystallographer.

We are also studying the use of the photon counting pixel detectors for the imaging of small animals. The idea is to associate X-ray radiography to a micro-PET (Positron Emission Tomography) in order to locate the surgical intervention tool with respect to the activated object.

## VIII. REFERENCES

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